



Technical Specifications

1. No. of Channels : 45 - TTL, HV or either edge (+/-) clock.[6 Nos.]/ -ve level. Protected ($\pm 10V$), Supplied with 5 sets of multicoloured pigtail double hook pin grabber with 10 pin rear end FFC connector supported on extension board.

CH. No.	1 - 27	28-35	36-39	40-45
Function	Regular Inputs / CH-9,18, 27 as trigger I/P	Regular Inputs / Pattern Generator O/P (0/4/8 bit) / 35 as trigger I/P	Regular Inputs / either edge clock	HV (Schmitt Inputs) / Trigger Inputs or Clock source

- Clock Source : 330 MHz generated using internal PLL with 33 MHz crystal or external clock (in all 14 sources) as per table above. Cannot overlap clock with trigger I/P.
- Display: 16 x 2 LCD w backlight, menu selection/acquire mode using 1 slide switch & 2 PBs.
- LEDs : P. ON + 4 status LEDs.
- Memory Depth : 2K/channel.
- Trigger Source : Total 10 channels acting as trigger inputs, rising or falling edge.
- Mnemonic based Disassembly Support : YES, optionally for 8051/8085 need processor Personality Module.
- PC Interface : Operates through parallel port of PC either built in or PCI card. [Optionally supplied]
- GUI : Through signal tap-II Software operates through JTAG interface based on parallel port or USB using Byte Blaster.
- Operating Temperature : 0-50°C
- External Power Supply : +5 V / 1 Amp. using external SMPS [240VAC]

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Specifications subject to change without notice