


**CPU : K80S series**

Item		Specifications	
Control method		Cyclic execution of stored program, Time-driven operation, Interrupt task execution	
I/O control method		Scan synchronized batch processing method (Refresh method)	
Program		Mnemonic, Ladder	
Number of instruction	Basic	30	
	Application	218	
Processing speed		0.5 $\mu$ S/Step	
Program capacity		7Ksteps	
Data memory	I/O Relay(P)	P0000~P31F (512 points)	
	Auxiliary Relay(M)	M0000~M191F (3,072 points)	
	Keep Relay(K)	K0000~K031F (512 points)	
	Link Relay(L)	L0000~L063F (1,024 points)	
	Special Relay(F)	F000~F063F (1,024 points)	
	Timer(T)	100ms	T000~T191 (192 points)
		10ms	T192~T255(64 points)
	Counter©	C000~C255 (256 points)	
	Step Controller(S)	S00.00~S99.99 (100 x 100)	
Data Register(D)	D0000~D4999 (5,000 words)		
Timer (5 Types)		ON Delay, OFF Delay, Accumulation, Monostable, Retriggerable Timer	
Counter (4 Types)		Up, Down, Up-Down, Ring counter	
Self-diagnosis functions		Watchdog timer, Memory error, I/O error, Batter error, Power supply error	
Built-in special function	PID control		Controlled by instruction, Auto tuning, Forward/Reverse action, Forced output, Operation scan time setup
	Cnet I/F function (RS-232C)		Dedicated protocol, MODBUS protocol, User-defined protocol
	High speed counter	Counting speed	1 Phase: 16kHz (1 channel)
			2 Phase: 8kHz (1 channel)
			3 Counting modes
		Counting method	1Phase, up/down count with program Input
	1Phase, up/down count B-phase Input		
	2 Phase, up/down with phase difference		
	Multiplication	1, 2 or 4	
	Pulse catch		Pulse width : 0.2 ms, 8points
Pulse output		2kHz	
External interrupt		8 points (0.4ms)	
Input filter		0~15ms (Setting by 1ms)	